

Notice of References CitedApplication/Control
09/855,433Applicant(s)/Patent Under
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	U	Antola et al., Optimizing high-level synthesis for self-checking arithmetic circuits, pg. 268-276
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.